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Doc description: Information Disclosure Statement (IDS) Filed

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number		10033027
Filing Date		2001-10-22
First Named Inventor	Warren Snyder	
Art Unit	2183	
Examiner Name	Idriss N. Airobaye	
Attorney Docket Number	CD00232M	

**U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6826689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6623282	B1	2004-11-23	Snyder	Entire Document
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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shutt et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
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15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
17	6868500	B1	2005-03-15	Kutz et al.	Entire Document
18	6859884	B1	2005-02-22	Sullam	Entire Document
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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
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24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
33	7185321	B1	2007-02-27	Roe et al.	Entire Document

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## **U.S. PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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## **FOREIGN PATENT DOCUMENTS**

Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	TS
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## **NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	TS
	1	USPTO Notice of Allowance for 10/238,966 (CD01250) dated 01/27/09; 4 pages	<input type="checkbox"/>



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2	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 06/30/08; 12 pages	<input type="checkbox"/>
3	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 12/28/07; 12 pages	<input type="checkbox"/>
4	USPTO Final Rejection for 10/238,966 (CD01250) dated 09/27/07; 9 pages	<input type="checkbox"/>
5	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/19/07; 7 pages	<input type="checkbox"/>
6	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 10/20/06; 8 pages	<input type="checkbox"/>
7	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
8	Application Number 09/964,991 (CD00196): "A Novel Band-Gap Circuit for Providing an Accurate Reference Voltage Compensated for Process State, Process Variations and Temperature," Kutz et al.; 25 pages	<input type="checkbox"/>
9	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar; 28 pages	<input type="checkbox"/>
10	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance," Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
11	"WP 3.5: An Integrated Time Reference," Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
12	"Micropower CMOS Temperature Sensor with Digital Output," Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>

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13	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 05/11/05; 10 pages	<input type="checkbox"/>
14	USPTO Final Rejection for 10/001,477 (CD01203) dated 10/24/05; 13 pages	<input type="checkbox"/>
15	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 03/02/06; 14 pages	<input type="checkbox"/>
16	USPTO Final Rejection for 10/001,477 (CD01203) dated 08/24/06; 13 pages	<input type="checkbox"/>
17	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 10/20/08; 18 pages	<input type="checkbox"/>
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20	USPTO Final Rejection for 10/001,478 (CD01213) dated 09/17/07; 15 pages	<input type="checkbox"/>
21	USPTO Non-Final Rejection for 10/001,478 (CD01213) dated 04/02/07; 17 pages	<input type="checkbox"/>
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24	USPTO Final Rejection for 10/001,478 (CD01213) dated 10/24/05; 15 pages	<input type="checkbox"/>
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28	USPTO Notice of Allowance for 10/002,217 (CD01207) dated 06/06/08; 7 pages	<input type="checkbox"/>
29	USPTO Final Rejection for 10/002,217 (CD01207) dated 02/06/08; 10 pages	<input type="checkbox"/>
30	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 08/03/07; 10 pages	<input type="checkbox"/>
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32	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 10/02/06; 21 pages	<input type="checkbox"/>
33	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 04/03/06; 12 pages	<input type="checkbox"/>
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35	USPTO Non-Final Rejection for 10/002,217 (CD01207) dated 05/19/05; 15 pages	<input type="checkbox"/>
36	USPTO Notice of Allowance for 10/001,477 (CD01203) dated 11/10/08; 7 pages	<input type="checkbox"/>
37	USPTO Advisory Action for 10/001,477 (CD01203) dated 10/10/08; 3 pages	<input type="checkbox"/>
38	USPTO Final Rejection for 10/001,477 (CD01203) dated 06/30/08; 15 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for 10/001,477 (CD01203) dated 12/06/07; 13 pages	<input type="checkbox"/>
40	USPTO Final Rejection for 10/001,477 (CD01203) dated 07/23/07; 16 pages	<input type="checkbox"/>
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## **EXAMINER SIGNATURE**

Examiner Signature	Date Considered
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<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 801.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.

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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
	5	5781747	B1	1998-07-14	Kametani	Entire Document
	6	6604179	B1	2003-08-05	Volk et al.	Entire Document
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9	6249167	B1	2001-06-19	Oguchi et al.	Entire Document
10	4134073	B1	1979-01-01	MacGregor	Entire Document
11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
12	6771552	B1	2004-08-03	Fujisawa	Entire Document
13	5371860	B1	1994-12-06	Mura et al.	Entire Document
14	6575373	B1	2003-06-10	Nakano	Entire Document
15	5710906	B1	1998-01-20	Ghosh et al.	Entire Document
16	6294962	B1	2001-09-25	Mar	Entire Document
17	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
18	5630102	B1	1997-05-13	Johnson et al.	Entire Document
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20	5331571	B1	1994-07-19	Aronoff et al.	Entire Document
21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
23	5659502	B1	1996-09-24	Schutte	Entire Document
24	5689196	B1	1997-11-18	Schutte	Entire Document
25	5670915	B1	1997-09-23	Cooper et al.	Entire Document
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31	5428319	B1	1995-06-27	Marvin et al.	Entire Document
32	5604466	B1	1997-02-18	Dreps et al.	Entire Document
33	5898345	B1	1999-04-27	Namura et al.	Entire Document
34	5304955	B1	1994-04-19	Atriss et al.	Entire Document
35	5870345	B1	1999-02-09	Stecker	Entire Document
36	5870004	B1	1999-02-09	Lu	Entire Document
37	5699024	B1	1997-12-16	Manlove et al.	Entire Document
38	5345195	B1	1994-09-06	Cordoba et al.	Entire Document
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42	4947169	B1	1990-08-07	Smith et al.	Entire Document
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**U.S.PATENT APPLICATION PUBLICATIONS**

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	1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
	2	PCT/US96/17305	WO	A1	1996-06-06	Schutte	Entire Document	<input type="checkbox"/>
	3	PCT/US98/34376	WO	A1	1998-08-06	Schutte	Entire Document	<input type="checkbox"/>

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	1	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/09/08; 34 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for 09/975,115 (CD00182) dated 10/31/06; 19 pages	<input type="checkbox"/>
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17	USPTO Non-Final Rejection for 09/975,338 (CD00186) dated 04/05/05; 13 pages	<input type="checkbox"/>
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34	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages	<input type="checkbox"/>
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Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

39	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 11/04/2004; 6 pages	<input type="checkbox"/>
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22	USPTO Final Rejection for 09/989,771 (CD01182) dated 02/27/07; 8 pages	<input type="checkbox"/>
23	USPTO Final Rejection for 09/989,771 (CD01182) dated 03/28/06; 9 pages	<input type="checkbox"/>
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	1	"Pod-Wikipedia, the free encyclopedia"; retrieved on November 14, 2005 from <a href="http://en.wikipedia.org/wiki/Pod">http://en.wikipedia.org/wiki/Pod</a> ; 3 pages	<input type="checkbox"/>
	2	"pod-definition by dict.die.net"; retrieved on November 14, 2005 from <a href="http://dict.die.net/pod">http://dict.die.net/pod</a> ; 2 pages	<input type="checkbox"/>
	3	"In-Circuit Emulators - descriptions of the major ICes around"; retrieved on November 14, 2005 from <a href="http://www.algonet.se/~staffann/developer/emulator.htm">http://www.algonet.se/~staffann/developer/emulator.htm</a> ; 6 pages	<input type="checkbox"/>
	4	CD00183; "Capturing Test/Emulation and Enabling Real-Time Debugging Using FPGA for In-Circuit Emulation," October 10, 2001; 09/975,104; Snyder; 35 pages	<input type="checkbox"/>
	5	USPTO Non-Final Rejection for 09/989,767 (CD01178M) dated 07/24/08; 21 pages	<input type="checkbox"/>
	6	Anonymous; "Using Debug"; 1999; Prentice-Hall Publishing; 20 pages	<input type="checkbox"/>
	7	Harrison et al.; "Xilinx FPGA Design in a Group Environment Using VHDS and Synthesis Tools". Colloquium on Digital System Design Using Synthesis Techniques; February 15, 1996; 4 pages	<input type="checkbox"/>

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

8	Microsoft Press Computer User's Dictionary; 1998; 3 pages	<input type="checkbox"/>
9	Sreeram Duvvuru and Siamak Arya, "Evaluation of a Branch Target Address Cache," 1995; IEEE; 8 pages	<input type="checkbox"/>
10	Andrew S. Tanenbaum with contributions from James R. Goodman, "Structured Computer Organization," 1999, Prentice Hall, Fourth Edition; 32 pages	<input type="checkbox"/>
11	CD00186; "Method for Breaking Execution of a Test Code in DUT and Emulator Chip Essentially Simultaneously and Handling Complex Breakpoint Events," October 10, 2001; 09/975,338; Nemecek et al.; 34 pages	<input type="checkbox"/>
12	CD00185; "Emulator Chip-Board Architecture for Interface," October 10, 2001; 09/975,030; Snyder et al.; 37 pages	<input type="checkbox"/>
13	Wikipedia - Main Page, retrieved on March 8, 2006 from <a href="http://en.wikipedia.org/wiki/Main_Page">http://en.wikipedia.org/wiki/Main_Page</a> and <a href="http://en.wikipedia.org/wiki/Wikipedia:Introduction">http://en.wikipedia.org/wiki/Wikipedia:Introduction</a> ; 5 pages	<input type="checkbox"/>
14	Wikipedia - Processor register, retrieved on March 7, 2006 from <a href="http://en.wikipedia.org/wiki/Processor_register">http://en.wikipedia.org/wiki/Processor_register</a> ; 4 pages	<input type="checkbox"/>
15	Jonathan B. Rosenberg, "How Debuggers Work" John Wiley & Sons, Inc. 1996; 269 pages	<input type="checkbox"/>
16	Dahl, et al.; "Emulation of the Sparcle Microprocessor with the MIT Virtual Wires Emulation System"; 1994; IEEE; 9 pages	<input type="checkbox"/>
17	Bauer et al.; "A Reconfigurable Logic Machine for Fast Event-Driven Simulation"; 1998; Design Automation Conference Proceedings; 8 pages	<input type="checkbox"/>
18	CD00184; "Host to FPGA Interface in an In-Circuit Emulation System," 10/10/01; 09/975,105; Nemecek; 44 pages	<input type="checkbox"/>

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19	USPTO Advisory Action for 09/994,601 dated 5/23/06 (CD01170M); 3 pages	<input type="checkbox"/>
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21	USPTO Ex Parte Quayle Action for 09/992,076 dated 06/18/07 (CD01210M); 6 pages	<input type="checkbox"/>
22	USPTO Non-Final Rejection for 09/992,076 dated 08/10/06 (CD01210M); 19 pages	<input type="checkbox"/>
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24	USPTO Notice of Allowance for 09/992,076 dated 07/29/08 (CD01210M); 6 pages	<input type="checkbox"/>
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27	USPTO Final Rejection for 09/989,778 (CD01172M) dated 02/05/07; 17 pages	<input type="checkbox"/>
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43	USPTO Final Rejection for 09/989,767 (CD01178M) dated 01/11/07; 12 pages	<input type="checkbox"/>
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**U.S. PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7188063	B1	2007-03-06	Snyder, Warren	Entire Document
	2	6002398	B1	1999-12-14	Wilson, Daniel	Entire Document
	3	6014135	B1	2000-01-11	Fernandes, Antonio	Entire Document
	4	6052524	B1	2000-04-18	Pauna, Mark	Entire Document
	5	6104325	B1	2000-08-15	Liaw, et al.	Entire Document
	6	6121965	B1	2000-09-19	Kenney, et al.	Entire Document
	7	6223144	B1	2001-04-24	Barnett, et al.	Entire Document
	8	6282551	B1	2001-08-28	Anderson, et al.	Entire Document

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Attorney Docket Number	CD00232M	

9	6304101	B1	2001-10-16	Nishihara, Yoshio	Entire Document
10	6384947	B1	2002-05-07	Ackerman, et al.	Entire Document
11	6438565	B1	2002-08-20	Ammirato, et al.	Entire Document
12	6456304	B1	2002-09-24	Angiulo, et al.	Entire Document
13	6592626	B1	2003-07-15	Bauchot, et al.	Entire Document
14	6613098	B1	2003-09-02	Sorge, et al.	Entire Document
15	6701508	B1	2004-03-02	Bartz, et al.	Entire Document
16	6711731	B1	2004-03-23	Weiss, Mitchell	Entire Document
17	6715132	B1	2004-03-30	Bartz, et al.	Entire Document
18	5329471	B1	1994-07-12	Swoboda, et al.	Entire Document
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20	5883623	B1	1999-03-16	Cseri, Istvan	Entire Document
21	5874958	B1	1999-02-23	Ludolph, Frank	Entire Document
22	4670838	B1	1987-06-02	Kawata	Entire Document
23	4939637	B1	1990-07-03	Pawloski	Entire Document
24	4969087	B1	1990-11-06	Tanagawa, et al.	Entire Document
25	5313618	B1	1994-05-17	Pawloski	Entire Document
26	5371878	B1	1994-12-06	Coker	Entire Document
27	5495593	B1	1996-02-27	Elmer, et al.	Entire Document
28	5574852	B1	1996-11-12	Baikker, et al.	Entire Document
29	5754552	B1	1998-05-19	Allmond, et al.	Entire Document
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31	5416895	B1	1995-05-16	Anderson, et al.	Entire Document
32	6072803	B1	2000-06-06	Allmond, et al.	Entire Document
33	6278568	B1	2001-08-21	Cloke, et al.	Entire Document
34	6371878	B1	2004-04-16	Bowen	Entire Document
35	7206733	B1	2007-04-17	Nemecek	Entire Document
36	6438738	B1	2002-08-20	Elayda	Entire Document
37	6631508	B1	2003-10-07	Williams	Entire Document
38	6658633	B1	2003-12-02	Devins, et al.	Entire Document
39	3378877	B1	2004-01-13	Perry, et al.	Entire Document
40	5852733	B1	1998-12-28	Chien, et al.	Entire Document
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42	5784545	B1	1998-07-21	Anderson, et al.	Entire Document
43	6121965	B1	2000-09-19	Kenny, et al.	Entire Document
44	6239798	B1	2001-05-29	Ludolph, et al.	Entire Document
45	6263339	B1	2001-07-17	Hirsh	Entire Document
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	7	USPTO Non-Final Rejection for 09/992,076 (CD01210M) dated 11/21/2005; 29 pages	<input type="checkbox"/>
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28	ITO, SERGIO AKIRA AND CARRO, LUIGI; "A Comparison of Microcontrollers Targeted to FPGA-Based Embedded Applications", September 2000, Proceedings of 13th Symposium on Integrated Circuits and Systems Design, 18-24 September 2000; 6 pages	<input type="checkbox"/>
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30	Monte Mar, Bert Sullam, Eric Blom; "An architecture for a configurable Mixed-signal device", 2003, IEEE Journal of Solid-State Circuits, Volume 3; 4 pages	<input type="checkbox"/>
31	ROBINSON, GORDON D; "Why 1149.1 (JTAG) Really Works", May 1994, Conference Proceedings Electro/94 International, 10-12 May 1994, Combined Volumes; 6 pages	<input type="checkbox"/>
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33	"PSoC designer: Integrated development environment, getting started 25-minute tutorial, version 1.0", Cypress Microsystems., Cypress Microsystems, Inc. CMS10006A, July 3, 2001; 25 pages	<input type="checkbox"/>
34	"PSoC technology complete changes 8-bit MCU system design", Cypress Microsystems, Inc. retrieved from <http://www.archive.org/web/20010219005250/http://cypressmicro.com/~rl...>, Feb. 19, 2001; 21 pages	<input type="checkbox"/>
35	Specks et al., "A Mixed Digital-Analog 16B Microcontroller with 0.5MB Flash Memory, On-Chip Power Supply, Physical Network Interface, and 40V I/O for Automotive Single-Chip Mechatronics," IEEE, Feb. 9, 2000, 1 page	<input type="checkbox"/>
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49	Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital);" Snyder; 13 pages	<input type="checkbox"/>

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	1	5050168	B1	1991-09-17	Paterson, Timothy L.	Entire Document
	2	6785881	B1	2004-08-31	Bartz et al.	Entire Document
	3	6587995	B1	2003-07-01	Duboc et al.	Entire Document
	4	5220512	B1	1993-06-15	Watkins et al.	Entire Document
	5	5878425	B1	1999-03-02	Redpath, Richard J.	Entire Document
	6	6970844	B1	2005-11-29	Bierenbaum, Steven E.	Entire Document
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( Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	TS
	1	USPTO Non-Final Rejection for 09/994,601 (CD01170M) dated 11/14/2006; 15 pages	<input type="checkbox"/>
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15	Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm;" Snyder; 26 pages	<input type="checkbox"/>
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Attorney Docket Number	CD00232M

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Examiner Signature		Date Considered	
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<sup>1</sup> See Kind Codes of USPTO Patent Documents at [www.USPTO.GOV](http://www.USPTO.GOV) or MPEP 901.04. <sup>2</sup> Enter office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>3</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>4</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>5</sup> Applicant is to place a check mark here if English language translation is attached.

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Attorney Docket Number	CD00232M

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20020109722	A1	2002-08-15	Rogers et al.	Entire Document
	2	20050143968	A1	2003-08-28	Odom et al.	Entire Document
	3	20020145433	A1	2002-10-10	Morrise et al.	Entire Document
	4	20030126947	A1	2003-07-10	Moore et al.	Entire Document
	5	20020174411	A1	2002-11-21	Feng et al.	Entire Document
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	7	20030025734	A1	2003-02-06	Boose et al.	Entire Document
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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

(Not for submission under 37 CFR 1.99)

Application Number		10033027
Filing Date		2001-10-22
First Named Inventor	Warren Snyder	
Art Unit	2183	
Examiner Name	Idriss N. Alrobye	
Attorney Docket Number	CD00232M	

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## **FOREIGN PATENT DOCUMENTS**

Examiner Initial*	Cite No	Foreign Document Number <sup>3</sup>	Country Code <sup>2</sup>	Kind Code <sup>4</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	Ts
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## **NON-PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	Ts
	1	"New Object Domain R3 Beta Now Available (Build 134)!" March 13, 2001; < <a href="http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html">http://web.archive.org/web/200100331202605/www.objectdomain.com/domain30/index.html</a> >; 2 pages	<input type="checkbox"/>
	2	"OMG XML Metadata Interchange (XMI) Specifications" 2000; 17 pages	<input type="checkbox"/>
	3	Electronic Tools Company; E-Studio User Manual; 2000; retrieved from <a href="http://web.archive.org">http://web.archive.org</a> for site <a href="http://e-tools.com">http://e-tools.com</a> on March 23, 2005; 77 pages	<input type="checkbox"/>
	4	Cover Pages Technology Reports; XML and Electronic Design Automation (EDA); August 2000; retrieved from <a href="http://xml.coverpages.org">http://xml.coverpages.org</a> on March 23, 2005; 5 pages	<input type="checkbox"/>
	5	Microsoft Computer Dictionary "ActiveX" 2002; Microsoft Press; 5th Edition; 3 pages	<input type="checkbox"/>
	6	Wikipedia "XLM" retrieved on January 29, 2007 from <a href="http://en.wikipedia.org/wiki/XML">http://en.wikipedia.org/wiki/XML</a> ; 16 pages	<input type="checkbox"/>

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7	"VHDL Samples" retrieved on January 29, 2007 from <a href="http://www.csee.umbc.edu/help/VHDL/samples/samples.shtml">http://www.csee.umbc.edu/help/VHDL/samples/samples.shtml</a> ; 10 pages	<input type="checkbox"/>
8	Anonymous, "Lotus Notes FAQ -- How do you generate unique document numbers?" September 19, 1999; retrieved from <a href="http://www.keysolutions.com">www.keysolutions.com</a> on July 9, 2008; 1 page	<input type="checkbox"/>
9	Ashok Bindra, "Programmable SoC Delivers A New Level Of System Flexibility"; Electronic Design; November 6, 2000; 11 pages	<input type="checkbox"/>
10	Cypress MicroSystem, Inc. "Cypres Customer Forums" retrieved from < <a href="http://www.cypress.com/forums/messageview">http://www.cypress.com/forums/messageview</a> >; 1 page	<input type="checkbox"/>
11	Cypress MicroSystem, Inc. "PsoC Designer: Integrated Development Environment User Guide"; Rev. 1.18; 9/8/2003; 193 pages	<input type="checkbox"/>
12	Hamblen, "Rapid Prototyping Using Field-Programmable Logic Devices" June 2000, IEEE; 9 pages	<input type="checkbox"/>
13	USPTO Final Rejection for Application Number 09/943,062 (CD00228) dated 04/30/2004; 9 pages	<input type="checkbox"/>
14	Snyder et al., "Xilinx's A-to-Z Systems Platform" Cahners Microprocessor, The Insider's Guide to Microprocessor Hardware, February 6, 2001; 6 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 01/27/2003; 7 pages	<input type="checkbox"/>
20	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 09/11/2002; 9 pages	<input type="checkbox"/>
21	"PSoC Designer: Integrated Development Environment" User Guide; Revision 1.11; Last Revised 07/17/2001; 109 pages	<input type="checkbox"/>
22	Cypress Microsystems, "Cypress Microsystems Unveils Programmable System-on-a-Chip for Embedded Internet, Communications and Consumer Systems," 2000, < <a href="http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html">http://www.cypressmicro.com/corporate/CY_Announces_nov_13_2000.html</a> >; 3 pages	<input type="checkbox"/>
23	Huang et al., "ICEBERG, An Embedded In-Circuit Emulator Synthesizer for Microcontrollers, Proceedings of the 36th Design Automation Conference 1999; 6 pages	<input type="checkbox"/>
24	Yoo et al., "Fast Hardware-Software Co-verification by Optimistic Execution of Real Processor," Proceedings of Design, Automation and Test in Europe Conference and Exhibition 2000; 6 pages	<input type="checkbox"/>
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26	USPTO Final Rejection for Application Number 09/943,062 (CD00228) dated 01/18/2008; 8 pages	<input type="checkbox"/>
27	USPTO Non-Final Rejection for Application Number 09/943,062 (CD00228) dated 06/22/2007; 12 pages	<input type="checkbox"/>
28	USPTO Miscellaneous Action for Application Number 09/943,062 (CD00228) dated 01/30/2006; 2 pages	<input type="checkbox"/>

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30	USPTO Non-Final Rejection for Application Number 10/113,064 (CD01200M) dated 04/06/2006; 19 pages	<input type="checkbox"/>
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31	USPTO Final Rejection for Application Number 10/113,064 (CD01200M) dated 10/18/2005; 22 pages	<input type="checkbox"/>
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32	USPTO Non-Final Rejection for Application Number 10/113,064 (CD01200M) dated 04/25/2005; 15 pages	<input type="checkbox"/>
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**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6714066	B1	2004-03-30	Gorecki et al.	Entire Document
	2	6453175	B1	2002-09-17	Mizell et al.	Entire Document
	3	6185127	B1	2001-02-06	Myers et al.	Entire Document
	4	6141376	B1	2000-10-31	Shaw	Entire Document
	5	5905398	B1	1999-05-18	Todsen et al.	Entire Document
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	8	6011407	B1	2000-01-04	New	Entire Document

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9	6066961	B1	2000-05-23	Lee et al.	Entire Document
10	5396245	B1	1995-03-07	Rempfer	Entire Document
11	3940760	B1	1976-02-01	Brokaw	Entire Document
12	5963075	B1	1999-10-05	Hiragizawa	Entire Document
13	6150866	B1	2000-11-21	Eto et al.	Entire Document
14	5307381	B1	1994-04-26	Ahuja	Entire Document
15	6211715	B1	2001-04-03	Terauchi	Entire Document
16	6600575	B1	2003-07-29	Kohara	Entire Document
17	4812684	B1	1989-03-14	Yamagiwa et al.	Entire Document
18	6754849	B1	2004-06-22	Tamura	Entire Document
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20	6728900	B1	2004-04-27	Meli	Entire Document
21	5544311	B1	1996-08-06	Harenberg et al.	Entire Document
22	5664199	B1	1997-09-02	Kuwahara	Entire Document
23	6175914	B1	2001-01-16	Mann	Entire Document
24	6574590	B1	2003-06-03	Kershaw et al.	Entire Document
25	5479652	B1	1995-12-26	Dreyer et al.	Entire Document
26	6356960	B1	2002-03-12	Jones et al.	Entire Document
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29	6681359	B1	2004-01-20	Au et al.	Entire Document
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31	6041406	B1	2000-03-21	Mann	Entire Document
32	5088822	B1	1992-02-18	Warren	Entire Document
33	5935266	B1	1999-08-10	Thurnhofer et al.	Entire Document
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36	6026501	B1	2000-02-15	Hohl et al.	Entire Document
37	6567932	B1	2003-05-20	Edwards et al.	Entire Document
38	6591369	B1	2003-07-08	Edwards et al.	Entire Document
39	6601189	B1	2003-07-29	Edwards et al.	Entire Document
40	6185703	B1	2001-02-06	Guddat et al.	Entire Document
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42	5875293	B1	1999-02-23	Bell et al.	Entire Document
43	5663965	B1	1997-09-02	Seymour	Entire Document
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45	6005814	B1	1999-12-21	Mulholland et al.	Entire Document
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47	5230000	B1	1993-07-20	Mozingo et al.	Entire Document
48	6061511	B1	2000-05-09	Martantz et al.	Entire Document
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50	6661288	B1	2003-12-09	Morgan et al.	Entire Document

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## **U.S.PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT** ( Not for submission under 37 CFR 1.99)

Application Number		10033027
Filing Date		2001-10-22
First Named Inventor	Warren Snyder	
Art Unit	2183	
Examiner Name	Idriss N. Alrobaye	
Attorney Docket Number	CD00232M	

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Examiner Initial*	Cite No	Patent Number	Kind Code <sup>1</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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## **FOREIGN PATENT DOCUMENTS**

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	1	USPTO Notice of Allowance for Application Number 09/875,599 (CD00180) dated 10/17/2006; 4 pages	<input type="checkbox"/>
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21	USPTO Final Rejection for Application Number 10/001,568 (CD01214M) dated 10/26/2005; 16 pages	<input type="checkbox"/>
22	USPTO Non-Final Rejection for Application Number 10/001,568 (CD01214M) dated 05/19/2005; 16 pages	<input type="checkbox"/>
23	USPTO Notice of Allowance for Application Number 09/887,955 (CD00197) dated 10/12/2004; 5 pages	<input type="checkbox"/>
24	USPTO Non-Final Rejection for Application Number 09/887,955 (CD00197) dated 05/26/2004; 5 pages	<input type="checkbox"/>
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27	USPTO Notice of Allowance for Application Number 09/893,048 (CD00231) dated 07/25/2006; 4 pages	<input type="checkbox"/>
28	USPTO Non-Final Rejection for Application Number 09/893,048 (CD00231) dated 01/12/2006; 6 pages	<input type="checkbox"/>
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36	USPTO Notice of Allowance for Application Number 09/909,047 (CD00173) dated 02/15/2005; 6 pages	<input type="checkbox"/>
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39	USPTO Non-Final Rejection for Application Number 09/912,768 (CD00200) dated 04/11/2005; 14 pages	<input type="checkbox"/>
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41	USPTO Non-Final Rejection for Application Number 09/912,768 (CD00200) dated 06/22/2004; 11 pages	<input type="checkbox"/>
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44	USPTO Notice of Allowance for Application Number 09/923,461 (CD00192) dated 05/12/2005; 6 pages	<input type="checkbox"/>
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46	USPTO Notice of Allowance for Application Number 09/929,891 (CD00226) dated 12/23/2005; 4 pages	<input type="checkbox"/>
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**U.S.PATENTS**

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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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## FOREIGN PATENT DOCUMENTS

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8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages	<input type="checkbox"/>
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19	Charles, Jr. et al., "Wirebonding: Reinventing the Process for MCMs," April 1998; IEEE 7th International Conference on Multichip Modules and High Density Packaging; 3 pages	<input type="checkbox"/>
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21	Song et al., "A 50% Power Reduction Scheme for CMOS Relaxation Oscillator," IEEE, 1999; 4 pages	<input type="checkbox"/>
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23	USPTO Final Rejection for Application Number 10/001,478 (CD01213M) dated 04/20/2009; 16 pages	<input type="checkbox"/>
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27	USPTO Non-Final Rejection for Application Number 09/989,771 (CD01182) dated 04/30/2009; 11 pages	<input type="checkbox"/>
28	USPTO Notice of Allowance for Application Number 09/989,762 (CD01175M) dated 03/25/2009; 7 pages	<input type="checkbox"/>
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Filing Date	2001-10-22
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Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

30	Application Number 09/989,815 (CD01166M): "A Data Driven Method and System for Monitoring Hardware Resource Usage for Programming an Electric Device;" Bartz et al.; 36 pages	<input type="checkbox"/>
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41	Application Number 09/998,859 (CD01209M): "A System and a Method for Checking Lock Step Consistency between in Circuit Emulation and a Microcontroller while Debugging Process is in Progress;" Nemecek; 33 pages	<input type="checkbox"/>
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11	7200507	B1	2007-04-03	Chen et al.	Entire Document
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	1	Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes;" Wright et al.; 33 pages	<input type="checkbox"/>
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14	Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan; 24 pages	<input type="checkbox"/>
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17	USPTO Non-Final Rejection for Application Number 11/132,894 (CD04064) dated 12/19/2006; 12 pages	<input type="checkbox"/>
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28	USPTO Non-Final Rejection for Application Number 10/305,589 (CD02181) dated 10/07/2003; 6 pages	<input type="checkbox"/>
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39	USPTO Non-Final Rejection for Application Number 10/871,582 (CD03036) dated 09/07/2005; 7 pages	<input type="checkbox"/>
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33	6980060	B1	2005-12-27	Boerstler et al.	Entire Document
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Application Number	10033027
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First Named Inventor	Warren Snyder
Art Unit	2183
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Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050280453	A1	2005-12-22	Hsieh	Entire Document

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## **FOREIGN PATENT DOCUMENTS**

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	1	Application Number 10/226,911 (CD02112): "Calibration of Integrated Circuit Time Constants." Gehring et al.; 32 pages	<input type="checkbox"/>
	2	USPTO Notice of Allowance for Application Number 09/943,149 (CD01085) dated 01/12/2004; 7 pages	<input type="checkbox"/>
	3	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 08/28/2003; 9 pages	<input type="checkbox"/>
	4	USPTO Final Rejection for Application Number 09/943,149 (CD01085) dated 05/07/2003; 10 pages	<input type="checkbox"/>
	5	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 11/20/2002; 7 pages	<input type="checkbox"/>
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8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
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10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
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12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages	<input type="checkbox"/>
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19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
20	Application Number 09/981,448 (CD01102): "Oscillator Tuning Method," Hauck; 28 pages	<input type="checkbox"/>
21	Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Krishnan; 27 pages	<input type="checkbox"/>
22	Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al.; 42 pages	<input type="checkbox"/>
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9	5392784	B1	1995-02-28	Gudaifis	Entire Document
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## **U.S.PATENT APPLICATION PUBLICATIONS**

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
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1	20030135842	A1	2003-07-17	Frey et al.	Entire Document
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	4	Application Number 11/200,619 (CD04112): "Providing hardware independence to automate code generation of processing device firmware," Snyder et al.; 41 pages	<input type="checkbox"/>
	5	USPTO Notice of Allowance for Application Number 11/201,922 (CD04111) dated 04/09/2009; 4 pages	<input type="checkbox"/>

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13	Application Number 11/201,627 (CD04109): "Method and an apparatus to design a processing system using a graphical user interface," Ogami et al.; 37 pages	<input type="checkbox"/>
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( Not for submission under 37 CFR 1.99)

Application Number	10033027
Filing Date	2001-10-22
First Named Inventor	Warren Snyder
Art Unit	2183
Examiner Name	Idriss N. Alrobaye
Attorney Docket Number	CD00232M

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